

transmitted through the universal asynchronous receiver/transmitter onto the state-displaying device. Such executive programs may include a detecting application program, a BIOS program to do the POST function (Power-On-Self-Test), and an operating system. Therefore, the application of the state-displaying device is unique in our current invention and is not the combined teaching of Benson's.

In sum, Benson's disclosure will not enable persons skilled in the art to teach or suggest achieve the same purpose or advantages of our current invention.

CLAIM AMENDMENT

In order to satisfy the Examiner's suggestion, the Applicant would like to make some amendments for the claims.

IN THE CLAIMS

Cancel Claims 14 and 15.

Amend Claims 1, 2, 3, 4, 6, 8, 11, 13 and 16.

Rewrite Claim 1 as follows:

1. A state-displaying device for displaying state data generated by a data-processing device, said state-displaying device comprises:

an universal asynchronous receiver/transmitter interface for receiving state data for outputting said state data in a serial mode; and

a displaying device including:

(a) a microprocessor coupled to said universal asynchronous receiver/transmitter interface for outputting a displaying signal in corresponding to said state data output by said universal asynchronous receiver/transmitter interface; and

(b) a multi-segment display module coupled to said microprocessor for displaying a symbol in corresponding to said displaying signal;

wherein said state data is generated by a BIOS program of said data-processing device
and/or said state data is generated by a detecting application program of said
data-processing device.

Rewrite Claim 2 as follows:

2. The state-displaying device as in claim 1, wherein said multi-segment display module [at least] is composed of at least one seven- segment display.

Rewrite Claim 3 as follows:

3. The state-displaying device as in claim 1, wherein said data-processing device is [selected between] a server [and] or a personal computer.

Rewrite Claim 4 as follows:

4. The state-displaying device as in claim 1, wherein said symbol is [selected among] a numeral, an English letter [and] or a specific character.

Rewrite Claim 6 as follows:

6. The state-displaying device as in claim 1, wherein said state data output by said universal asynchronous receiver/ transmitter interface [is of a] complies the specification

of RS-232.

Rewrite Claim 8 as follows:

8. The state-displaying device as in claim 1, wherein said state data includes an on/off bit, at least [a] one command mode bit and a plurality of displaying bits, said command mode bit is used to define a mode of displaying of said displaying bits, said microprocessor decides a mode of displaying of said multi- segment display module according to said mode of displaying of said displaying bits.

Rewrite Claim 11 as follows:

11. The state-displaying device as in claim 10, wherein said symbol is [selected among] a numeral, an English letter [and] or a specific character.

Rewrite Claim 13 as follows:

13. The state-displaying device as in claim 12, wherein said symbol is [selected among] a numeral, an English letter [and] or a specific character.

Rewrite Claim 16 as follows:

14 [16] The state-displaying device as in claim 1 [15], wherein said detecting application program is executed in an operating system of said data-processing device.

CLEAN VERSION OF AMENDED CLAIMS

1. A state-displaying device for displaying state data generated by a data-processing device,

said state-displaying device comprises:

an universal asynchronous receiver/transmitter interface for receiving state data for outputting said state data in a serial mode; and

a displaying device including:

(a) a microprocessor coupled to said universal asynchronous receiver/transmitter interface for outputting a displaying signal in corresponding to said state data output by said universal asynchronous receiver/transmitter interface; and

(b) a multi-segment display module coupled to said microprocessor for displaying a symbol in corresponding to said displaying signal;

wherein said state data is generated by a BIOS program of said data-processing device and/or said state data is generated by a detecting application program of said data-processing device.

2. The state-displaying device as in claim 1, wherein said multi-segment display module is

composed of at least one seven- segment display.

3. The state-displaying device as in claim 1, wherein said data-processing device is a server

and a personal computer.

4. The state-displaying device as in claim 1, wherein said symbol is a numeral, an

English letter or a specific character.

5. The state-displaying device as in claim 1, wherein said universal asynchronous

receiver/transmitter interface includes a data transmitting line Tx, a data receiving

line Rx, a power line and a grounding line (Gnd).

6. The state-displaying device as in claim 1, wherein said state data output by said universal asynchronous receiver/ transmitter interface complies the specification of RS-232.
7. The state-displaying device as in claim 1, wherein said state-displaying device is connected externally to a serial port.
8. The state-displaying device as in claim 1, wherein said state data includes an on/off bit, at least one command mode bit and a plurality of displaying bits, said command mode bit is used to define a mode of displaying of said displaying bits, said microprocessor decides a mode of displaying of said multi- segment display module according to said mode of displaying of said displaying bits.
9. The state-displaying device as in claim 8, wherein said command mode bit is used to decide between a searching mode and a following-the-sequence mode.

10. The state-displaying device as in claim 9, wherein when said microprocessor decides that a mode of displaying of said multi-segment display module is said searching mode, it makes said multi-segment display module to display said symbol according to said displaying signal which is generated by searching in a table according to values of said displaying bits.

11. The state-displaying device as in claim 10, wherein said symbol is a numeral, an English letter or a specific character.

12. The state-displaying device as in claim 9, wherein when said microprocessor decides that a mode of displaying is said following-the-sequence mode, a selecting bit of said displaying bits is used to designate a seven-segment display to be enabled, and said enabled seven-segment display is rendered to display said symbol according to the state of a plurality of segment-selecting bits of said displaying bits.

13. The state-displaying device as in claim 12, wherein said symbol is a numeral, an English letter or a specific character.

14. The state-displaying device as in claim 1, wherein said detecting application program is executed in an operating system of said data-processing device.

Should you have any question please contact Applicant's agent by mail or by phone at

(510) 579-6211. Thank you for your review.

Respectively yours,



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